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L34 L30 and I11

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135 L33

<u>L32</u> L30 and I8	242	<u>L32</u>
<u>L31</u> L30 and I6	352	<u>L31</u>
<u>L30</u> I2 and (plur\$7 or multip\$7 or second or two) near4 counter\$1	3333	<u>L30</u>
<u>L29</u> L25 and I11	83	<u>L29</u>
<u>L28</u> L25 and I10	588	<u>L28</u>
<u>L27</u> L25 and I8	771	<u>L27</u>
<u>L26</u> L25 and I6	1172	<u>L26</u>
<u>L25</u> I2 and counters	15432	<u>L25</u>
<u>L24</u> I2 and counter\$1	15444	<u>L24</u>

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<u>L20</u> I2 and I6	2146	<u>L20</u>
<u>L19</u> I4 and I11	0	<u>L19</u>
<u>L18</u> I4 and I10	0	<u>L18</u>
<u>L17</u> I4 and I8	1	<u>L17</u>
<u>L16</u> I4 and I6	1	<u>L16</u>
<u>L15</u> I5 and I11	8	<u>L15</u>
<u>L14</u> I5 and I10	70	<u>L14</u>
<u>L13</u> I5 and I8	86	<u>L13</u>
<u>L12</u> I5 and I6	144	<u>L12</u>
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<u>L10</u> (718/102-108)[CCLS]	4445	<u>L10</u>
<u>L9</u> (712/31-230,245-248)![CCLS]	9267	<u>L9</u>
<u>L8</u> (712/31-230,245-248)[CCLS]	9267	<u>L8</u>
<u>L7</u> (712/2-300)[CCLS]	12968	<u>L7</u>
<u>L6</u> (712/2-300)![CCLS]	12968	<u>L6</u>

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<u>L4</u> (correspond\$3 or repectiv\$4 or each or local) near6 count\$3 near80 I1	28	<u>L4</u>
<u>L3</u> (correspond\$3 or repectiv\$4 or each or local) near6 count\$3 near45 I1	26	<u>L3</u>

<u>L2</u>	(concurrent\$5 or parallel\$5 or simultaneous\$4) and l1 (workload\$1 or load\$1 or work\$1 or wight\$1 or task\$1 or process\$2)	62291	<u>L2</u>
<u>L1</u>	near15 (ALLOCAT\$6 OR SHAR\$6 OR ASSIGN\$5 OR DISTRIBUT\$3) near15 (plur\$7 or multip\$6 or multi\$6 or dual or two or second)	95897	<u>L1</u>

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 Slye, J.H.; Elnozahy, E.N.;
[Computers, IEEE Transactions on](#)
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 Digital Object Identifier 10.1109/12.729794
[AbstractPlus](#) | [References](#) | Full Text: [PDF](#)(120 KB) IEEE JNL
[Rights and Permissions](#)
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 Tseng, J.H.; Asanovic, K.;
[Computers, IEEE Transactions on](#)
 Volume 54, Issue 6, Jun 2005 Page(s):741 - 751
 Digital Object Identifier 10.1109/TC.2005.88
[AbstractPlus](#) | Full Text: [PDF](#)(1272 KB) IEEE JNL
[Rights and Permissions](#)
- ☐ 3. **Implementation of Hardware Multithreading in a Pipelined Processor**
 Manjikian, N.;
[Circuits and Systems, 2006 IEEE North-East Workshop on](#)
 June 2006 Page(s):145 - 148
 Digital Object Identifier 10.1109/NEWCAS.2006.250918
[AbstractPlus](#) | Full Text: [PDF](#)(95 KB) IEEE CNF
[Rights and Permissions](#)
- ☐ 4. **Methods for modeling resource contention on simultaneous multithreading processors**
 Moseley, T.; Kihm, J.L.; Connors, D.A.; Grünwald, D.;
[Computer Design: VLSI in Computers and Processors, 2005. ICCD 2005. Proceedings. 2005 IEEE Conference on](#)
 2-5 Oct. 2005 Page(s):373 - 380
 Digital Object Identifier 10.1109/ICCD.2005.74
[AbstractPlus](#) | Full Text: [PDF](#)(280 KB) IEEE CNF
[Rights and Permissions](#)
- ☐ 5. **Learning-Based SMT Processor Resource Distribution via Hill-Climbing**
 Seungryul Choi; Yeung, D.;
[Computer Architecture, 2006. 33rd International Symposium on](#)
 17-21 June 2006 Page(s):239 - 251
 Digital Object Identifier 10.1109/ISCA.2006.25

[AbstractPlus](#) | Full Text: [PDF\(816 KB\)](#) IEEE CNF

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- ☐ 6. **Integrating multiple forms of multithreaded execution on multi-SMT systems: a study with s**
Curtis-Maury, M.; Tanping Wang; Antonopoulos, C.; Nikolopoulos, D.;
[Quantitative Evaluation of Systems, 2005. Second International Conference on the](#)
19-22 Sept. 2005 Page(s):199 - 208
Digital Object Identifier 10.1109/QEST.2005.16

[AbstractPlus](#) | Full Text: [PDF\(344 KB\)](#) IEEE CNF
[Rights and Permissions](#)
- ☐ 7. **SMART: a simulation tool for analyzing cache access behavior on SMPs**
Li, T.; Gemdt, M.;
[Modeling, Analysis, and Simulation of Computer and Telecommunication Systems, 2005. 13th IEEE Symposium on](#)
27-29 Sept. 2005 Page(s):525 - 528
Digital Object Identifier 10.1109/MASCOTS.2005.66

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Hatfield, B.; Zhang, M.; Jin, L.;
[Frontiers in Education, 2004. FIE 2004. 34th Annual](#)
2004 Page(s):F3C - F12 Vol. 2
Digital Object Identifier 10.1109/FIE.2004.1408621

[AbstractPlus](#) | Full Text: [PDF\(519 KB\)](#) IEEE CNF
[Rights and Permissions](#)
- ☐ 9. **Responsive Multithreaded Processor for distributed real-time control**
Yamasaki, N.;
[Advanced Motion Control, 2004. AMC '04. The 8th IEEE International Workshop on](#)
25-28 March 2004 Page(s):457 - 462
Digital Object Identifier 10.1109/AMC.2004.1297912

[AbstractPlus](#) | Full Text: [PDF\(1661 KB\)](#) IEEE CNF
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- ☐ 10. **Microarchitectural denial of service: Insuring microarchitectural fairness**
Grunwald, D.; Ghiasi, S.;
[Microarchitecture, 2002. \(MICRO-35\). Proceedings. 35th Annual IEEE/ACM International Symposium](#)
18-22 Nov. 2002 Page(s):409 - 418
Digital Object Identifier 10.1109/MICRO.2002.1176268

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- ☐ 11. **A multithreaded concurrent garbage collector parallelizing the new instruction in Java**
Lo, C.-T.D.; Srisa-an, W.; Chang, J.M.;
[Parallel and Distributed Processing Symposium... Proceedings International, IPDPS 2002, Abstract](#)
15-19 April 2002 Page(s):59 - 64
Digital Object Identifier 10.1109/IPDPS.2002.1015550

[AbstractPlus](#) | Full Text: [PDF\(213 KB\)](#) IEEE CNF
[Rights and Permissions](#)
- ☐ 12. **MPX: Software for multiplexing hardware performance counters in multithreaded programs**
May, J.M.;
[Parallel and Distributed Processing Symposium... Proceedings 15th International](#)
23-27 April 2001 Page(s):8 pp.
Digital Object Identifier 10.1109/IPDPS.2001.924955

[AbstractPlus](#) | Full Text: [PDF\(156 KB\)](#) IEEE CNF

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- ☐ **13. Monotonic counters: a new mechanism for thread synchronization**
Thornley, J.; Mani Chandy, K.;
[Parallel and Distributed Processing Symposium, 2000. IPDPS 2000. Proceedings. 14th International](#)
1-5 May 2000 Page(s):573 - 582
Digital Object Identifier 10.1109/IPDPS.2000.846037
[AbstractPlus](#) | Full Text: [PDF](#)(168 KB) IEEE CNF
[Rights and Permissions](#)
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Blumofe, R.D.; Leiserson, C.E.;
[Foundations of Computer Science, 1994. Proceedings., 35th Annual Symposium on](#)
20-22 Nov. 1994 Page(s):356 - 368
Digital Object Identifier 10.1109/SFCS.1994.365680
[AbstractPlus](#) | Full Text: [PDF](#)(1140 KB) IEEE CNF
[Rights and Permissions](#)
- ☐ **15. Proposed object-oriented architecture of a flexible small-scale system for digital pulse shap**
Guazzoni, P.; Riccio, F.; Russo, S.; Sassi, M.; Zetta, L.;
[Nuclear Science, IEEE Transactions on](#)
Volume 53, Issue 3, Part 1, June 2006 Page(s):886 - 892
Digital Object Identifier 10.1109/TNS.2006.875067
[AbstractPlus](#) | Full Text: [PDF](#)(656 KB) IEEE JNL
[Rights and Permissions](#)
- ☐ **16. Thread-parallel MPEG-2, MPEG-4 and H.264 video encoders for SoC multi-processor archite**
Jacobs, T.R.; Chouliaras, V.A.; Mulvaney, D.J.;
[Consumer Electronics, IEEE Transactions on](#)
Volume 52, Issue 1, Feb. 2006 Page(s):269 - 275
Digital Object Identifier 10.1109/TCE.2006.1605057
[AbstractPlus](#) | Full Text: [PDF](#)(251 KB) IEEE JNL
[Rights and Permissions](#)
- ☐ **17. Parallel Implementation of EDAs Based on Probabilistic Graphical Models**
Mendiburu, A.; Lozano, J.A.; Miguel-Alonso, J.;
[Evolutionary Computation, IEEE Transactions on](#)
Volume 9, Issue 4, Aug. 2005 Page(s):406 - 423
Digital Object Identifier 10.1109/TEVC.2005.850299
[AbstractPlus](#) | Full Text: [PDF](#)(1232 KB) IEEE JNL
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- ☐ **18. Thread-parallel MPEG-4 and H.264 coders for system-on-chip multi-processor architectures**
Jacobs, T.R.; Chouliaras, V.A.; Mulvaney, D.J.;
[Consumer Electronics, 2006. ICCE '06. 2006 Digest of Technical Papers. International Conference](#)
7-11 Jan. 2006 Page(s):91 - 92
Digital Object Identifier 10.1109/ICCE.2006.1598325
[AbstractPlus](#) | Full Text: [PDF](#)(86 KB) IEEE CNF
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- ☐ **19. A case study of selected SPLASH-2 applications and the SBT debugging tool**
Novillo, E.; Lu, P.;
[Parallel and Distributed Processing Symposium, 2003. Proceedings, International](#)
22-26 April 2003 Page(s):8 pp.
Digital Object Identifier 10.1109/IPDPS.2003.1213519
[AbstractPlus](#) | Full Text: [PDF](#)(291 KB) IEEE CNF
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- ☐ **20. Transparent distributed threads for Java**
Haumacher, B.; Moschny, T.; Reuter, J.; Tichy, W.F.;
[Parallel and Distributed Processing Symposium, 2003. Proceedings. International](#)
22-26 April 2003 Page(s):7 pp.
Digital Object Identifier 10.1109/IPDPS.2003.1213261
[AbstractPlus](#) | Full Text: [PDF](#)(356 KB) IEEE CNF
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- ☐ **21. On-line debugging and performance monitoring with barriers**
Novillo, E.; Lu, P.;
[Parallel and Distributed Processing Symposium... Proceedings 15th International](#)
23-27 April 2001 Page(s):8 pp.
Digital Object Identifier 10.1109/IPDPS.2001.924959
[AbstractPlus](#) | Full Text: [PDF](#)(128 KB) IEEE CNF
[Rights and Permissions](#)
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Wakeman, I.; Jeffrey, A.; Owen, T.;
[Open Architectures and Network Programming, 2000. Proceeding. OPENARCH 2000, 2000 IEEE](#)
26-27 March 2000 Page(s):128 - 137
Digital Object Identifier 10.1109/OPNARC.2000.828140
[AbstractPlus](#) | Full Text: [PDF](#)(712 KB) IEEE CNF
[Rights and Permissions](#)
- ☐ **23. Datarol: a massively parallel architecture for functional languages**
Amamiya, M.; Taniguchi, R.;
[Parallel and Distributed Processing, 1990. Proceedings of the Second IEEE Symposium on](#)
9-13 Dec. 1990 Page(s):726 - 735
Digital Object Identifier 10.1109/SPDP.1990.143635
[AbstractPlus](#) | Full Text: [PDF](#)(732 KB) IEEE CNF
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